Nepal college of information technology

Unit Test

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| Level: Bachelor | Semester – Fall | Year : 2014 | |
| Programme: BE CE-V | | Full Marks : 70 | |
|  | | Pass Marks: 35 | |
| Course: Computer Architecture | | Time : 2hrs. | |
| *Candidates are required to give their answers in their own words as far as practicable.* | | |
| *The figures in the margin indicate full marks.* | | |
| *Attempt all the questions.* | | |

1. a) Differentiate between Computer Organization and Architecture with example. 7

b) What is the importance of using different types of addressing modes in computer system? Describe them in brief. 8

2) a) To develop todays system some principles are to be considered. What are the principles? 8

b) Explain how data transfer takes place in registers with necessary example. 7

3. a) What is instruction set? Draw the instruction cycle state diagram to execute an instruction. 8

b) Perform multiplication of -7\*3 using booth algorithm. 7

4. a) Explain how numbers are represented in ALU. Explain integer representation in detail. 7

b) Divide -5/3 using appropriate algorithm. 8

5. Write Short notes on (any two): 10

a) Processor organization

b) VHDL

c) Instruction Format

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